STATES PATENT AND TRADEMARK OFFICE PATENT Date: August 29, 2002 blication No.: 09/779,096 Applicant: Shi-Tron LIN, et al. Examiner: Ori Nadav Group Art Unit 2811 Filed: 02/08/2001 Our Ref: B-4101 (618582-4/RPB:TGC) For: "Output Buffer with Good ESD Protection" Hon. Commissioner of Patents and Trademarks Washington, D.C. 20231 Sir:

This paper is filed in response to the Office Action dated May 29, 2002. Please amend the specification and the claims as indicated below. All amendments and remarks herein are made without prejudice.

IN THE DRAWINGS

Applicant encloses herewith a letter to the Official Draftsperson requesting approval for corrected Figures 1A and 1B to overcome the Examiner's objection to the drawings.

IN THE ABSTRACT

Please replace the current abstract with the amended abstract set forth below. (Appendix B, which is enclosed herewith, shows how the current abstract was amended to produce the abstract set forth below.)